

ES 250 2nd Midterm Exam - Fall 2013

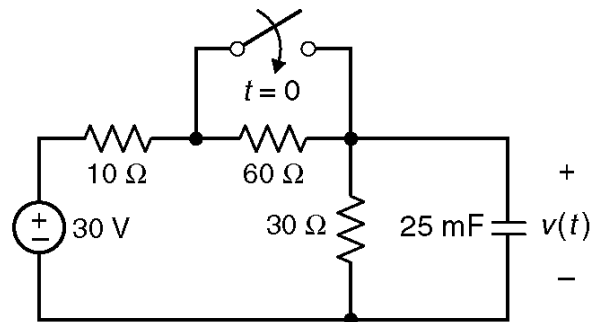
Name _____ k2 _____

Student # _____

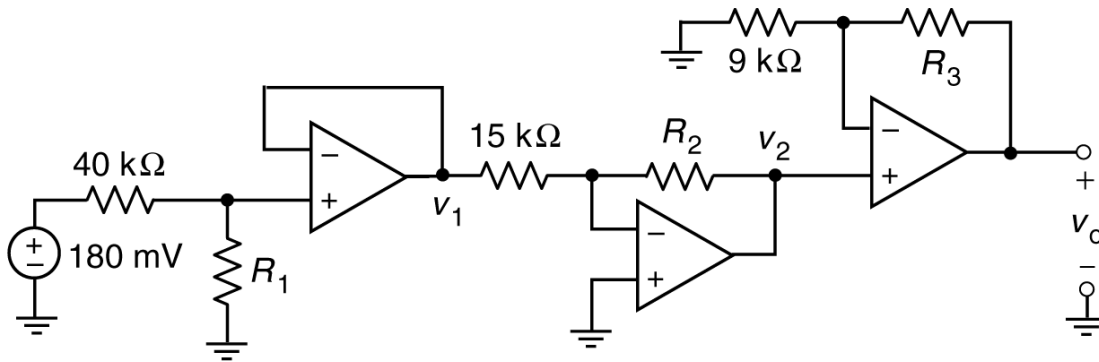
1. The switch in this circuit closes at time $t = 0$. Let $v(0)$ denote the capacitor voltage when the switch is open and the circuit is at steady state. Similarly, let $v(\infty)$ denote the steady state capacitor voltage when the switch is closed.

Determine the values of $v(0)$ and $v(\infty)$:

$$v(0) = \underline{9} \text{ V and } v(\infty) = \underline{22.5} \text{ V.}$$



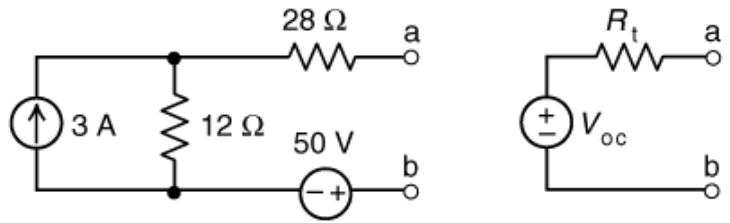
2.



The values of the node voltages v_1 , v_2 and v_o , are $v_1 = 80 \text{ mV}$, $v_2 = -320 \text{ mV}$ and $v_o = -960 \text{ mV}$. Determine the value of the resistances R_1 , R_2 and R_3 :

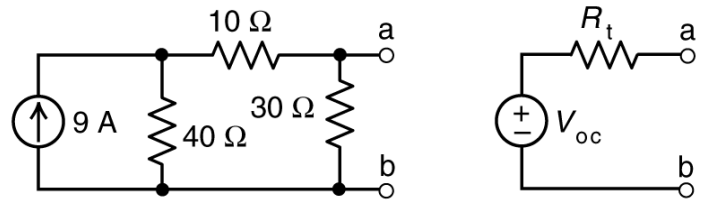
$$R_1 = \underline{32} \text{ k}\Omega, \quad R_2 = \underline{60} \text{ k}\Omega \text{ and } R_3 = \underline{18} \text{ k}\Omega.$$

3. Here's a circuit and its Thevenin equivalent circuit. Determine the values of the Thevenin resistance, R_t , and of the open-circuit voltage, V_{oc} .



$$R_t = \underline{40} \ \Omega \text{ and } V_{oc} = \underline{-14} \ \text{V}$$

4. Here's a circuit and its Thevenin equivalent circuit. Determine the values of the Thevenin resistance, R_t , and of the open-circuit voltage, V_{oc} .



$$R_t = \underline{18.75} \ \Omega \text{ and } V_{oc} = \underline{135} \ \text{V}$$

5. Given that $0 \leq R \leq \infty$ in this circuit, and given these two observations:

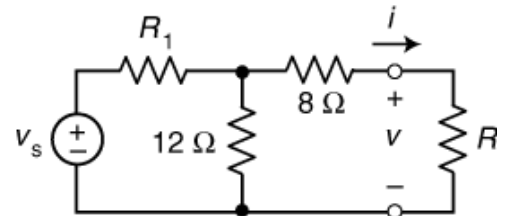
When $R = 0$ then $i = 1.5 \text{ A}$.

When $R = \infty$ then $v = 24 \text{ V}$.

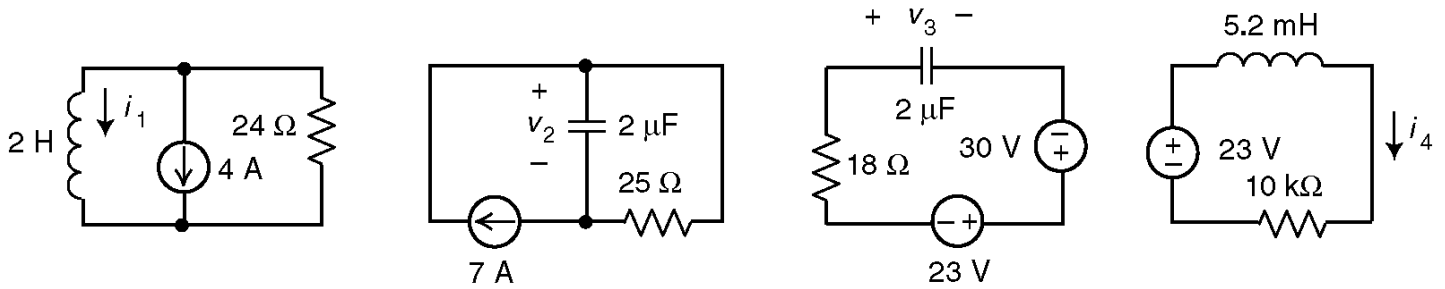
Fill in the blanks in the following statements:

a) When $R = \underline{48} \ \Omega$ then $v = 18 \text{ V}$.

b) When $R = \underline{44} \ \Omega$ then $i = 0.4 \text{ A}$.

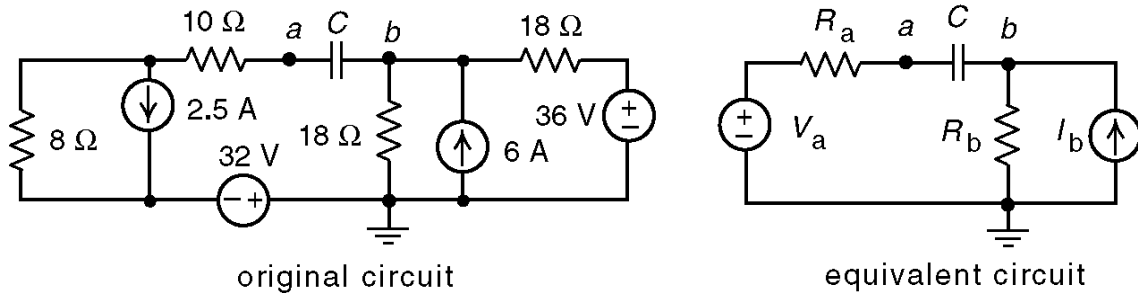


6. Here are 4 separate dc circuits. Because they are dc circuits, the capacitors in these circuits act like open circuits and the inductors act like short circuits. Determine the values of i_1 , v_2 , v_3 and i_4 .



$i_1 = \underline{-4} \text{ A}$, $v_2 = \underline{175} \text{ V}$, $v_3 = \underline{7} \text{ V}$ and $i_4 = \underline{2.3} \text{ mA}$.

7.



The equivalent circuit on the right is obtained from the original circuit on the left using source transformations and equivalent resistances. (The lower case letters a and b identify the nodes of the capacitor in both the original and equivalent circuits.) Determine the values of R_a , V_a , R_b and I_b in the equivalent circuit:

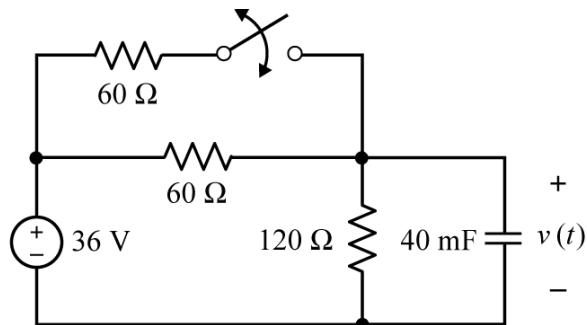
$R_a = \underline{18} \text{ } \Omega$, $V_a = \underline{-52} \text{ V}$, $R_b = \underline{9} \text{ } \Omega$ and $I_b = \underline{8} \text{ A}$.

8. a) Determine the time constant, τ , and the steady state capacitor voltage, $v(\infty)$, when the switch is **open**:

$$\tau = \underline{\quad 1.6 \quad} \text{ s} \quad \text{and} \quad v(\infty) = \underline{\quad 24 \quad} \text{ V}$$

b) Determine the time constant, τ , and the steady state capacitor voltage, $v(\infty)$, when the switch is **closed**:

$$\tau = \underline{\quad 0.96 \quad} \text{ s} \quad \text{and} \quad v(\infty) = \underline{\quad 28.8 \quad} \text{ V}$$

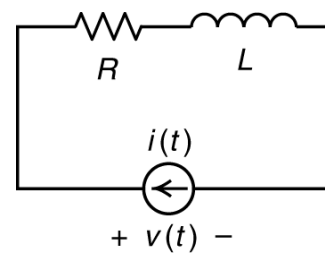


9. The input to this circuit is the current: $i(t) = 5 + 2e^{-7t}$ A for $t > 0$

The output is the voltage: $v(t) = 40 - 68e^{-7t}$ V for $t > 0$

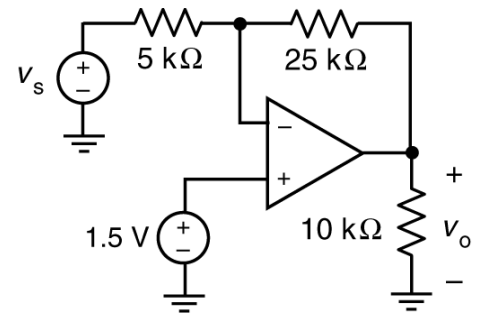
Determine the values of the resistance and inductance:

$$R = \underline{\quad 8 \quad} \Omega \quad \text{and} \quad L = \underline{\quad 6 \quad} \text{ H.}$$



10. The input to this circuit is the voltage v_s . The output is the voltage v_o . The output is related to the input by the equation $v_o = m v_s + b$ where m and b are constants. The values of m and b are:

$$m = \underline{-5} \text{ V/V and } b = \underline{9} \text{ V.}$$

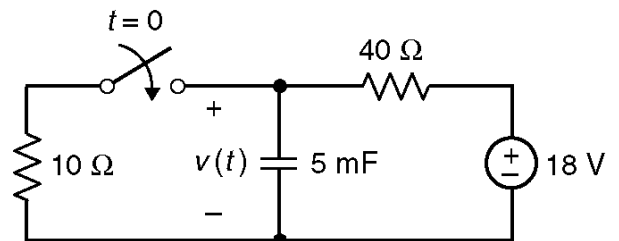


11. This circuit is at steady state before the switch closes. The capacitor voltage can be represented as

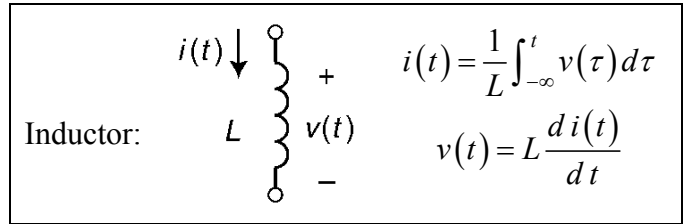
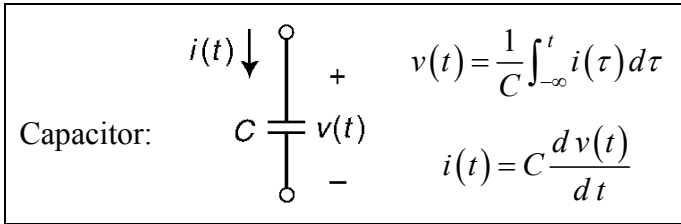
$$v(t) = A + B e^{-at} \text{ V for } t > 0$$

Determine the values of the real constants A , B and a :

$$A = \underline{3.6} \text{ V, } B = \underline{14.4} \text{ V and } a = \underline{25} \text{ 1/s.}$$

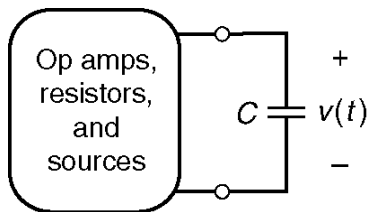


Element Equations

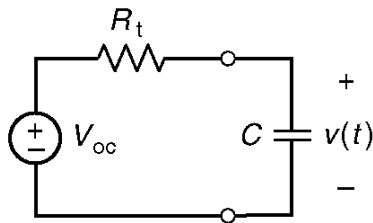


First-Order Circuits

FIRST-ORDER CIRCUIT CONTAINING A CAPACITOR



Replace the circuit consisting of op amps, resistors, and sources by its Thévenin equivalent circuit:



The capacitor voltage is:

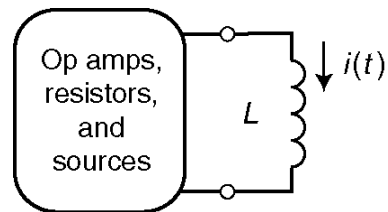
$$v(t) = V_{oc} + (v(0) - V_{oc}) e^{-\frac{t}{\tau}}$$

where the time constant, τ , is

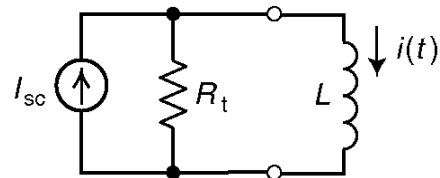
$$\tau = R_t C$$

and the initial condition, $v(0)$, is the capacitor voltage at time $t = 0$.

FIRST-ORDER CIRCUIT CONTAINING AN INDUCTOR



Replace the circuit consisting of op amps, resistors, and sources by its Norton equivalent circuit:



The inductor current is

$$i(t) = I_{sc} + (i(0) - I_{sc}) e^{-\frac{t}{\tau}}$$

where the time constant, τ , is

$$\tau = \frac{L}{R_t}$$

and the initial condition, $i(0)$, is the inductor current at time $t = 0$.